

AXOP36062/1S

60V RRIO Operational Amplifiers



Datasheet – Apr 2023

Description

The AXOP3606x is a high voltage (3V to 60V) operational amplifier (opamp) series with rail-to-rail input and output swing capabilities. These devices are very suitable for applications where high voltage operation, a small footprint, and high capacitive load drive are required. AXOP36061S is single opamp with Shutdown function. AXOP36062 is dual opamp without Shutdown function.

Features

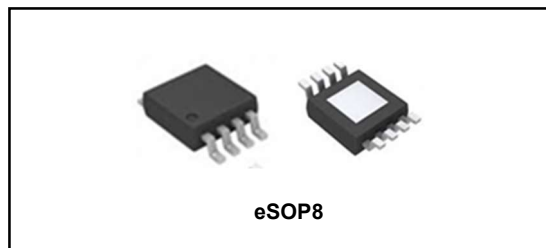
- Supply voltages from 3V to 60V
- Rail-to-rail input and output
- Low input offset voltage: $\pm 1\text{mV}$ typ
- Unity-gain bandwidth: 17MHz
- Slew rate: $15\text{V}/\mu\text{s}$
- THD 105dB
- SNR 105dB
- Low quiescent current (per opamp): 2.2mA typ @60V
- Easier to stabilize with higher capacitive load due to resistive open-loop output impedance
- Shutdown function (AXOP36061S only)

Applications

- Test equipment
- High voltage regulators and power amplifiers
- Industrial control
- Piezo drivers
- Active filters
- High-V current sensing
- Data acquisition system

Table 1 Device Summary

Order code	Package	Packing
AXOP36061S	eSOP8	Tube
AXOP36062	eSOP8	Tube



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1 Block Diagram and Application Circuit

Figure 1 Block Diagram

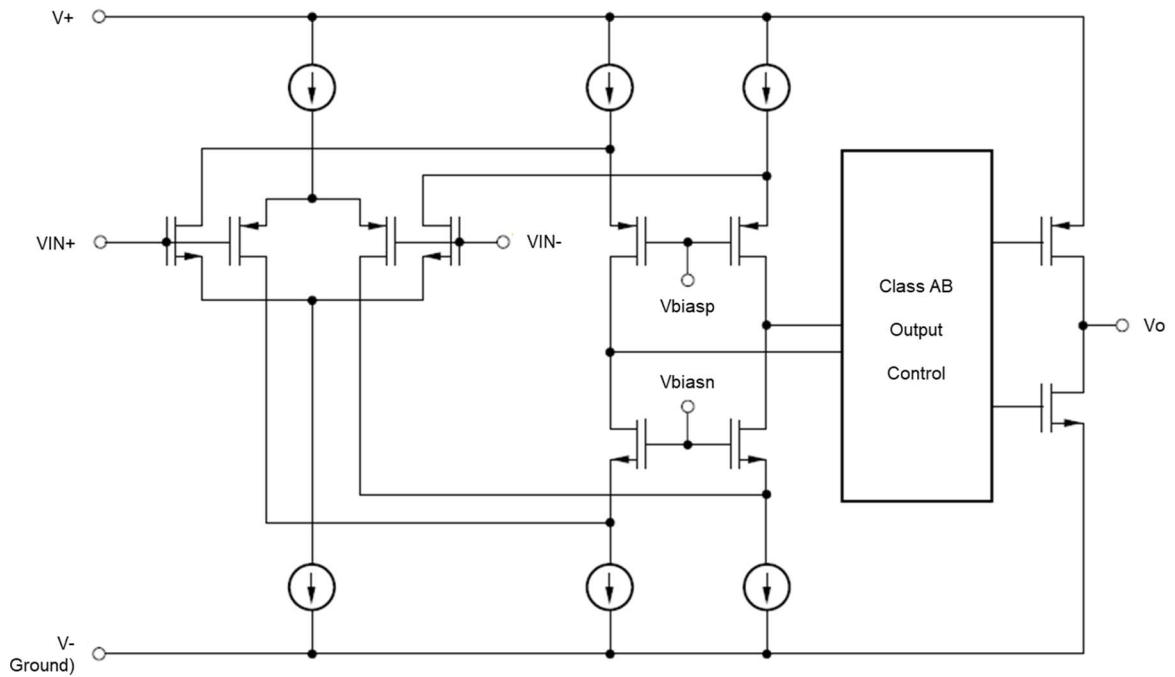
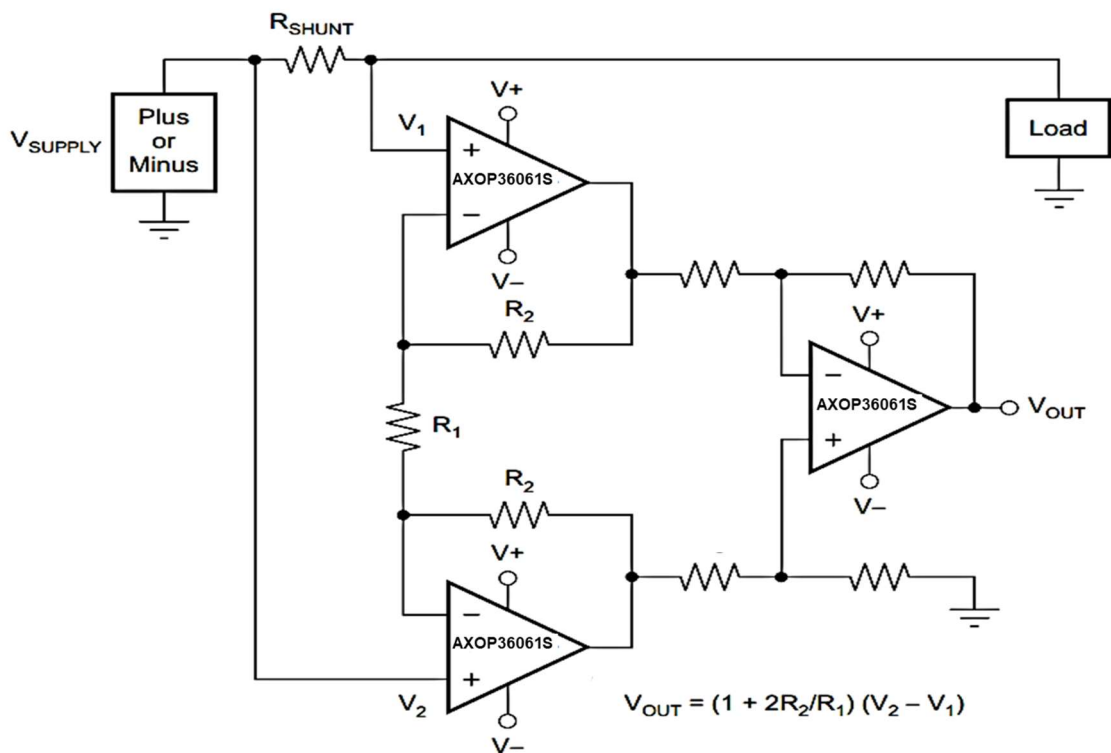


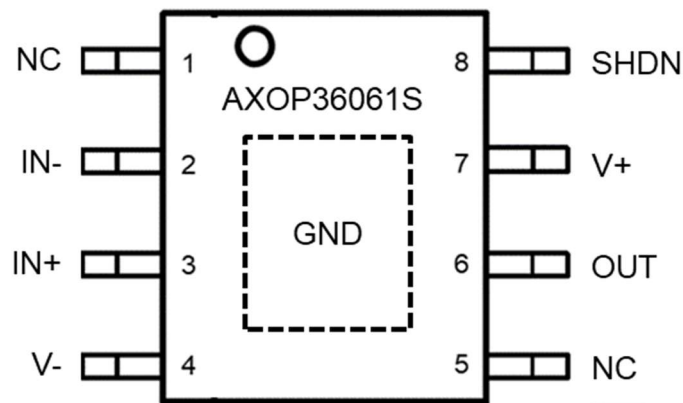
Figure 2 Typical Application Circuit (High Voltage High Side Current Sense)



2 Pin Description

2.1 AXOP36061S Pinouts

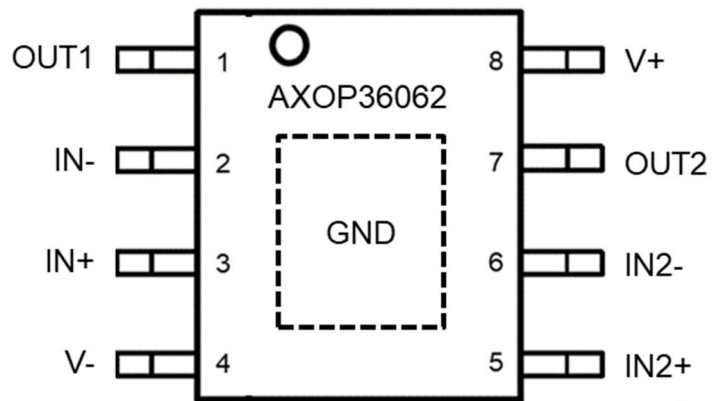
Figure 3 AXOP36061S Pinouts



Pin number	Pin name	Description
1	NC	Non connected pin
2	IN-	Inverting input
3	IN+	Non-inverting input
4	V-	Negative supply or ground
5	NC	Non connected pin
6	OUT	Output
7	V+	Positive supply
8	SHDN	Shutdown: "High" = opamp disabled Shutdown: "Low" = opamp enabled Shutdown: "Float" = opamp enabled

2.2 AXOP36062 Pinouts

Figure 4 AXOP36062 Pinouts



Pin number	Pin name	Description
1	OUT1	Output 1
2	IN1-	Inverting input 1
3	IN1+	Non-inverting input 1
4	V-	Negative supply or ground
5	IN2+	Non-inverting input 2
6	IN2-	Inverting input 2
7	OUT2	Output 2
8	V+	Positive supply

3 Electrical Specifications

3.1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V _s	Supply voltage (V ₊) - (V ₋)	-0.3 to +70	V
IN+, IN-, SHDN	Input pin voltage	(V ₋) - 0.5 to (V ₊) +0.5	V
OUT	Output pin voltage	(V ₋) - 0.5 to (V ₊) +0.5	V
T _j	Junction temperature	150	°C
T _{stg}	Storage temperature	-55 to +150	°C

3.2 Thermal Data

Table 3 Thermal Data

Symbol	Parameter	Value	Unit
R _{th j-case}	Thermal resistance junction-to-case Max.	10	°C/W

3.3 ESD

Table 4 ESD

Symbol	Parameter	Value	Unit
All pins	ESD (HBM)	±2,000	V
	ESD (CDM)	±500	V

3.4 Electrical Characteristics

For $V_s = (V_+) - (V_-) = 60V$ at $T_a = 25^\circ C$, R_L connected to $V_s/2$, $V_{cm} = V_s/2$, and $V_{out} = V_s/2$ (unless otherwise noted).

Table 5 Electrical Characteristics

Symbol	Parameter	Test condition	Min	Typ	Max	Unit
V_s	Supply voltage (V_+) - (V_-)		3		60	V
T_a	Operating ambient temperature		-40		85	$^\circ C$
Power Supply						
I_q	Quiescent current	$V_s=60V, I_o=0mA$		2.2	3.0	mA
		all temp			4.0	
Offset Voltage						
V_{os}	Input offset voltage			± 1	± 3	mV
		all temp			± 5	mV
dV_{os}/dT	Drift	all temp		± 2		$\mu V/^\circ C$
PSRR	Power-supply rejection ratio	At DC		120		dB
Input Voltage Range						
V_{cm}	Common mode voltage range	$V_s=5V$ to $60V$	$(V_-)-0.1$		$(V_+)+0.1$	V
CMRR	Common mode rejection ratio	At DC		120		dB
Input Bias Current						
I_b	Input bias current			± 0.5		pA
I_{os}	Input offset current			± 0.05		pA
Noise						
E_n	Input voltage noise	$f=20Hz$ to $20kHz$		5		μV
Input Capacitance						
C_{id}	Differential			2		pF
C_{ic}	Common mode			4		pF
Open Loop Gain						
A_{ol}	Open loop voltage gain			130		dB
Frequency Response						
GBP	Gain bandwidth product	$G=+1, C_L=10pF$		17		MHz
C_{load}	Capacitive load	$G=+1$			1	nF
SR	Slew rate	$G=+1, C_L=100pF$		15		$V/\mu s$
T_s	Settling time	To 0.1%, 30V step, $G=+1, C_L=100pF$		2		μs
THD	Total harmonic distortion	$V_s=60V, V_{cm}=30V, V_o=58V_{pp}, G=+1, f=1kHz$		105		dB

SNR	Signal to Noise Ratio	$V_{in}=1V_{rms}$, $G=+1$, $f=1kHz$		105		dB
Output						
V_o	Voltage output swing from supply rails	$R_L=10k\Omega$		0.5	1.0	V
Isc	Short circuit current	AXOP36061S		± 30 (± 100)		mA
		AXOP36062		± 50		mA
Shutdown (AXOP36061S only)						
Iqsd	Quiescent current per amplifier	$V_s=3V$ to 60V, amplifier disabled, SHDN = "High"		80	120	μA
Vsd	Shutdown threshold	$V_s=3V$ to 60V, amplifier disabled, SHDN = "High"	4			V
		$V_s=3V$ to 60V, amplifier enabled, SHDN = "Float" or SHDN = "Low"			1	V
ton	Amplifier enable time	$V_s=3V$ to 60V, full shutdown; $G=+1$, $V_o = 0.9 \times V_s/2$, R_L connected to V_-		2		μs
toff	Amplifier disable time	$V_s=3V$ to 60V, $G=+1$, $V_o=0.1 \times V_s/2$, R_L connected to V_-		2		μs

Disable time (toff) and enable time (ton) are defined as the time interval between the 50% point of the signal applied to the SHDN pin and the point at which the output voltage reaches the 10% (disable) or 90% (enable) level.

3.5 Typical Electrical Characteristics

Figure 5 Vos Distribution

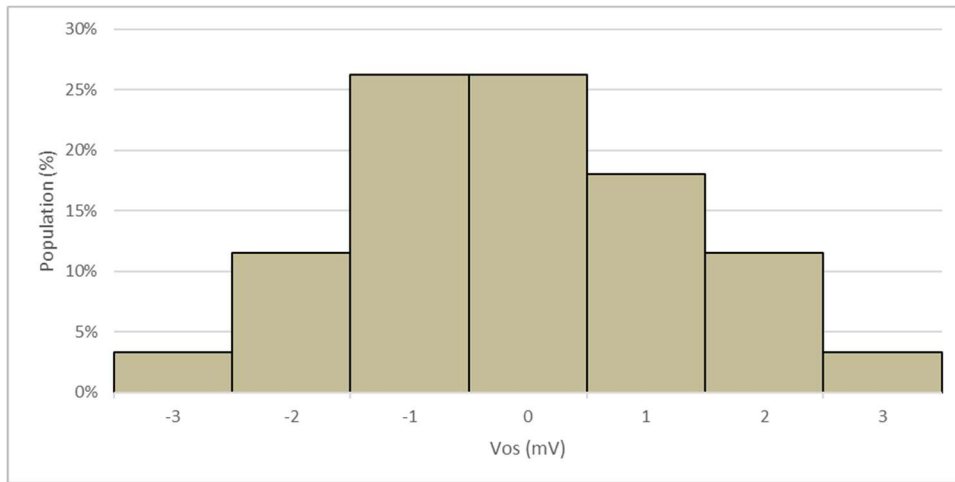


Figure 6 Vos vs Input Common Mode Voltage

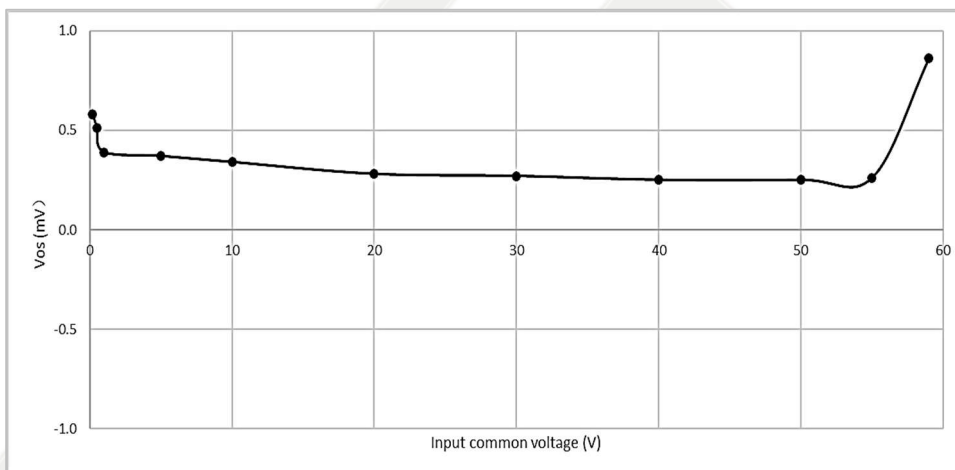


Figure 7 Vos vs Vs

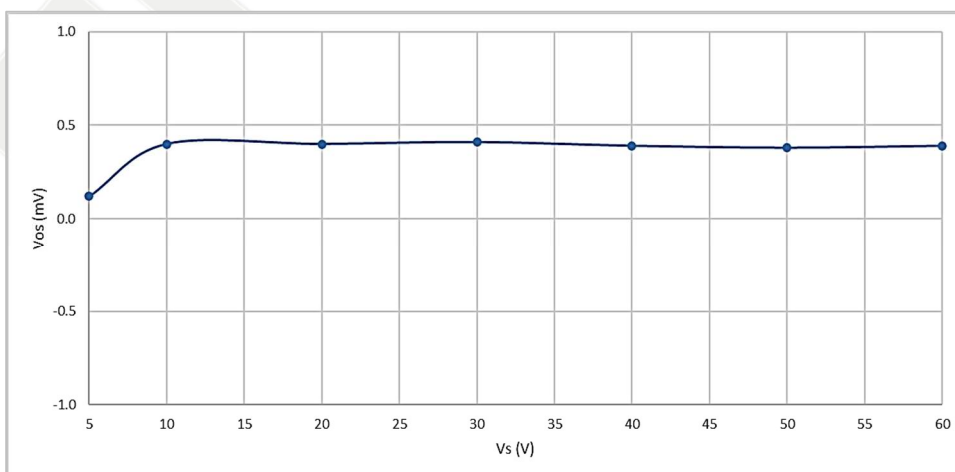


Figure 8 Iq (per opamp) vs Input Common Mode Voltage

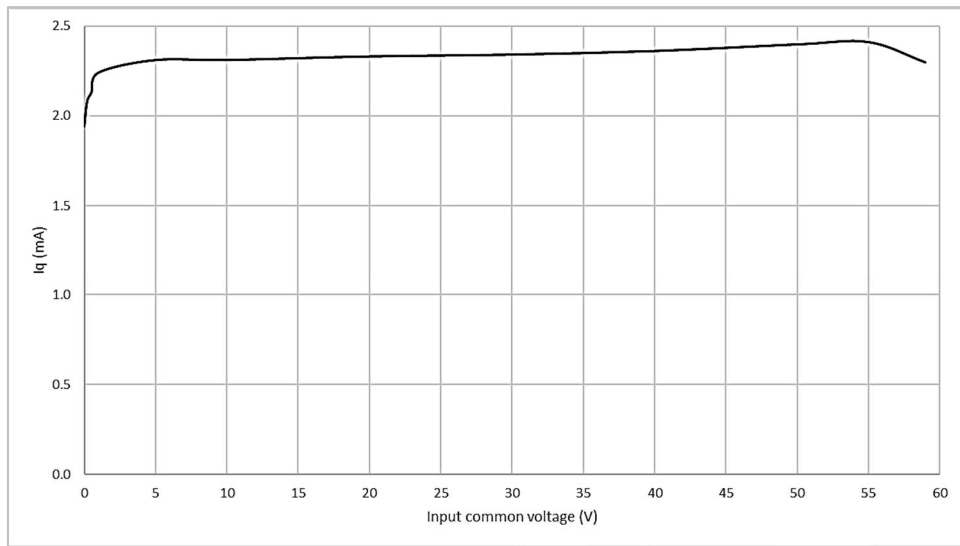


Figure 9 Iq (per opamp) vs Vs

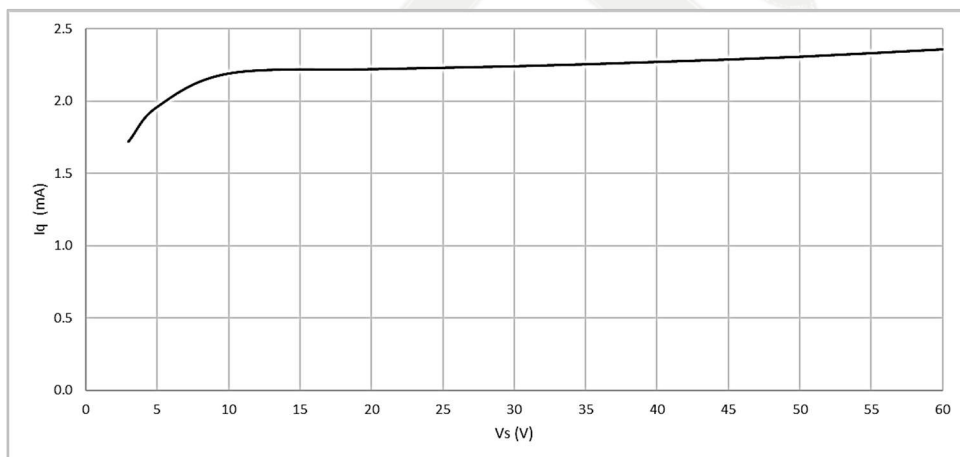


Figure 10 THD+N vs Frequency

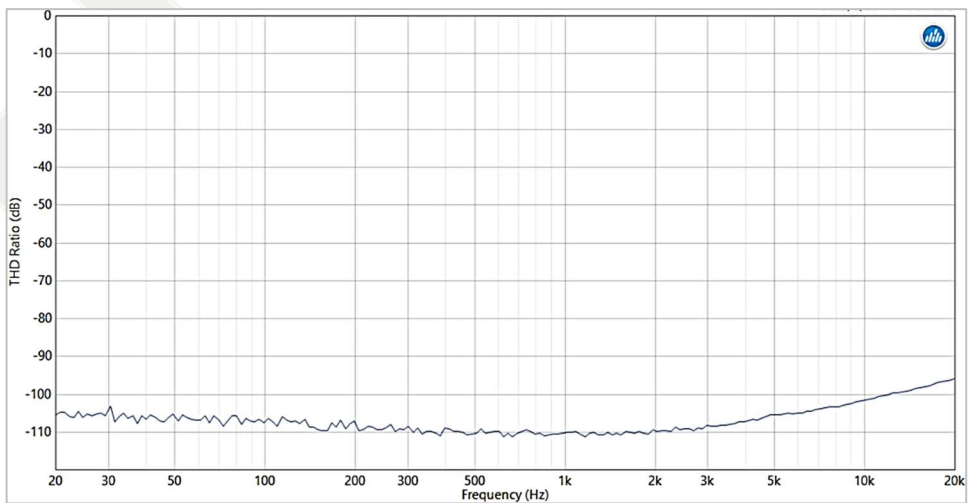


Figure 11 Large Signal Step Response

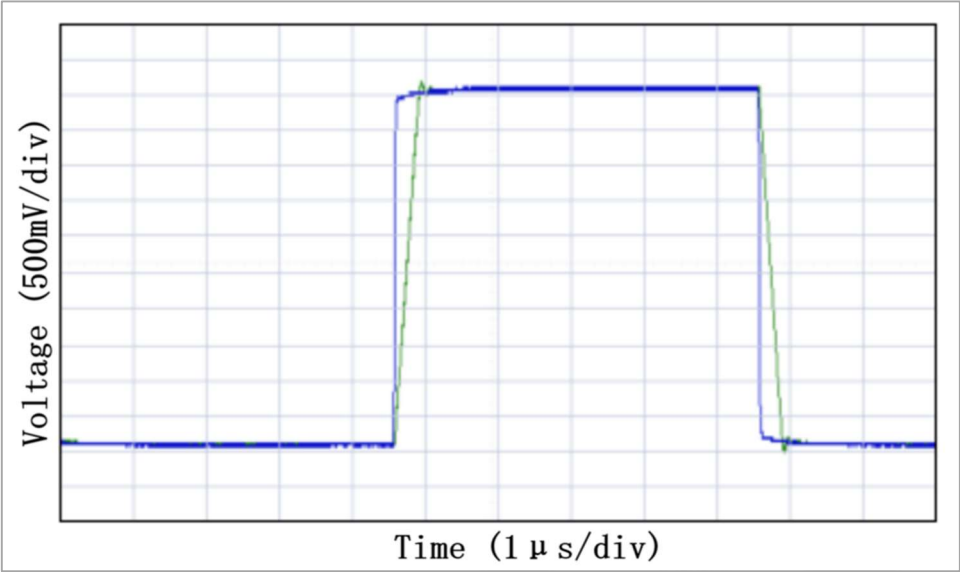
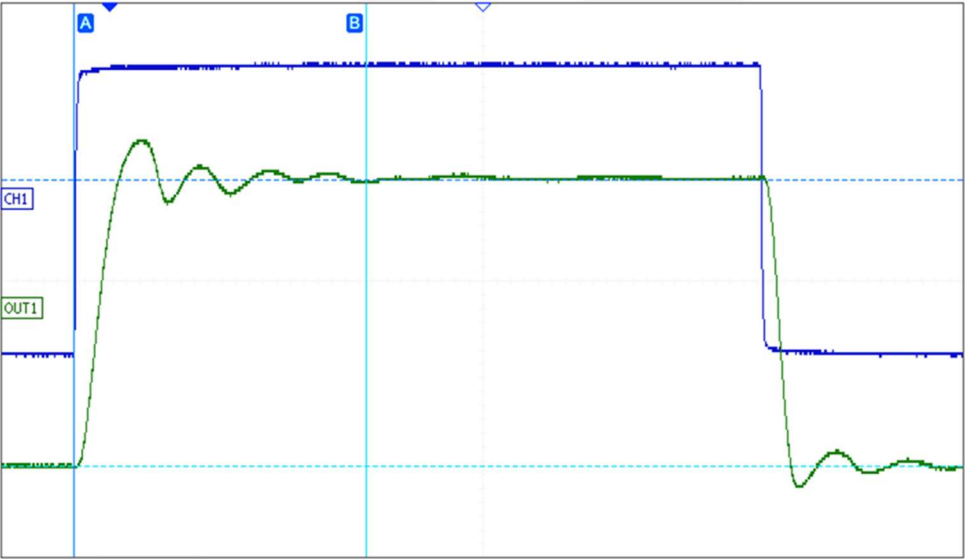


Figure 12 Sep Response with CL=1nF



4 Functional Description

4.1 Overview

The AXOP3606X is a 60V high voltage, rail-to-rail input and output opamp series. It operates from 3V to 60V, are unity gain stable, and are designed for a wide range of applications and used in virtually any single supply application.

4.2 Rail to Rail Input

The input common mode voltage range of AXOP3606X extends 100mV beyond the supply rails for the full supply voltage range of 3V to 60V. This performance is achieved with a complementary input stage: a N-channel input differential pair in parallel with a P-channel differential pair, as shown in Figure 1. The N-channel pair is active for input voltages close to the positive rail, typically $(V^+) - 1.4V$ to 200mV above the positive supply, whereas the P-channel pair is active for inputs from 200mV below the negative supply to approximately $(V^+) - 1.4V$. There is a transition region, in which both pairs are on. Within this transition region, PSRR, CMRR, offset voltage, offset drift, and THD can degrade compared to device operation outside this region.

4.3 Rail to Rail Output

Designed as a high voltage operational amplifier, AXOP3606X delivers a robust output drive capability. A class AB output stage with common source Mosfets achieves full rail-to-rail output swing capability. For resistive loads of 10k Ω , the output swings to within 0.5V (typ) of either supply rail, regardless of the applied power supply voltage. Different load conditions change the ability of the amplifier to swing close to the rails.

4.4 EMI Rejection

The AXOP3606X uses integrated electromagnetic interference (EMI) filtering to reduce the effects of EMI from sources such as wireless communications and densely populated boards with a mix of analog signal chain and digital components.

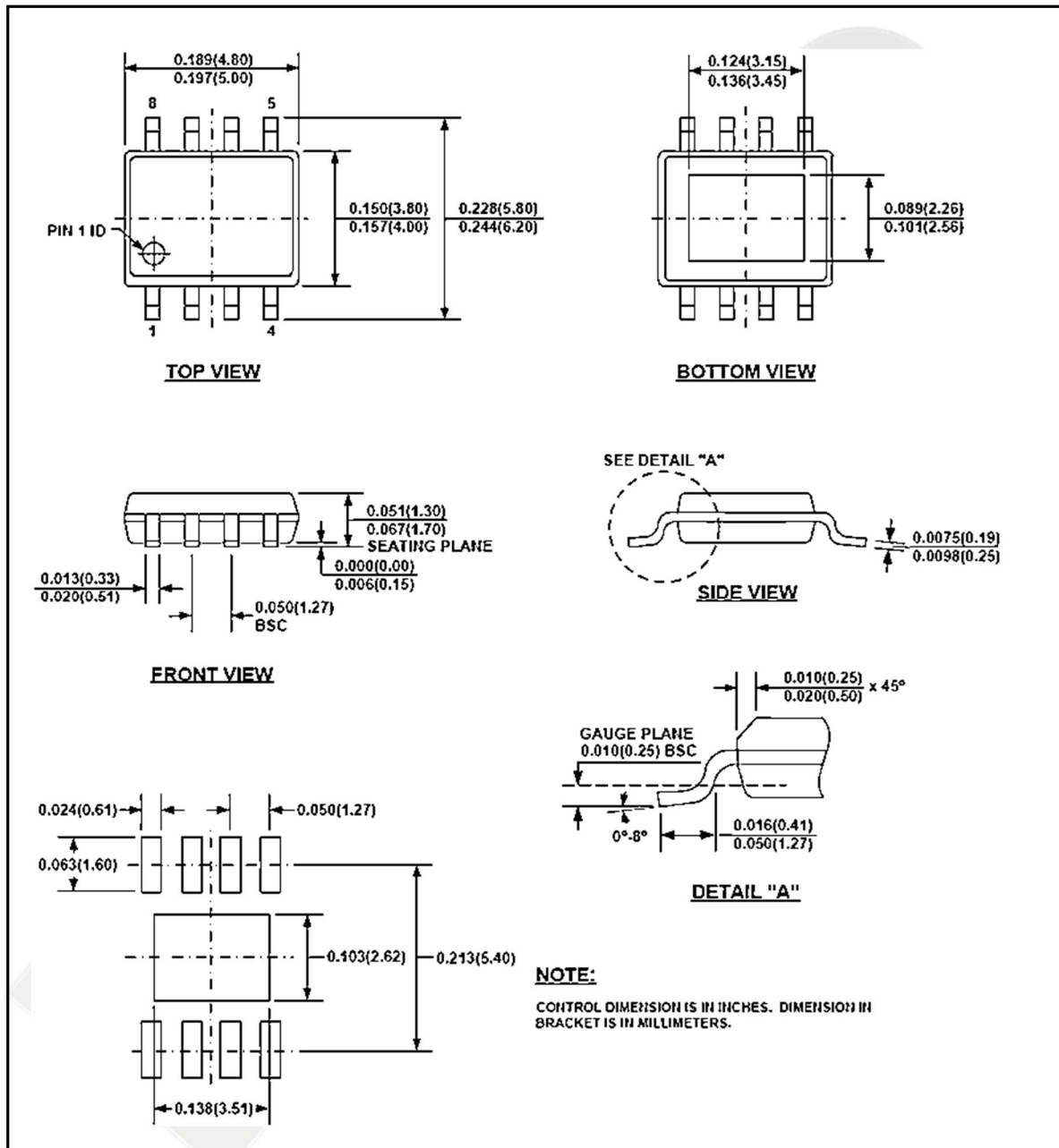
4.5 Shutdown

The AXOP36061S has shutdown function. The amplifiers can be shut down by pulling the shutdown (SHDN) pin to "High".

5 Package Information

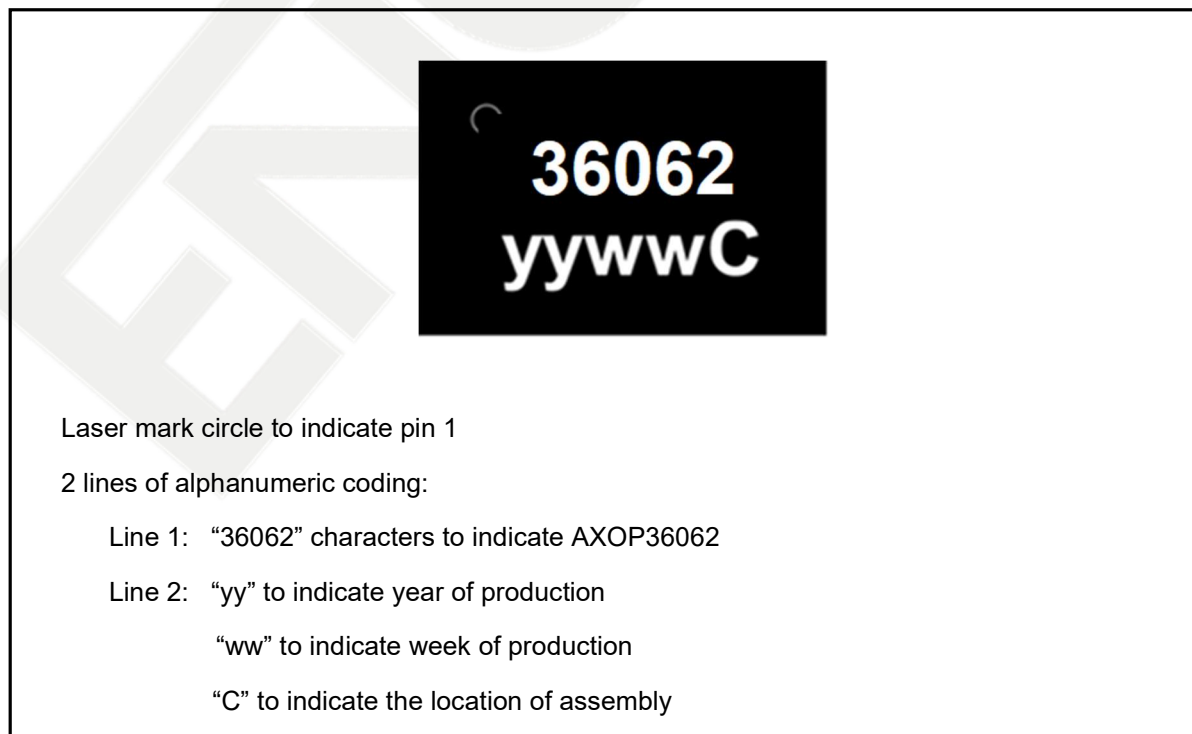
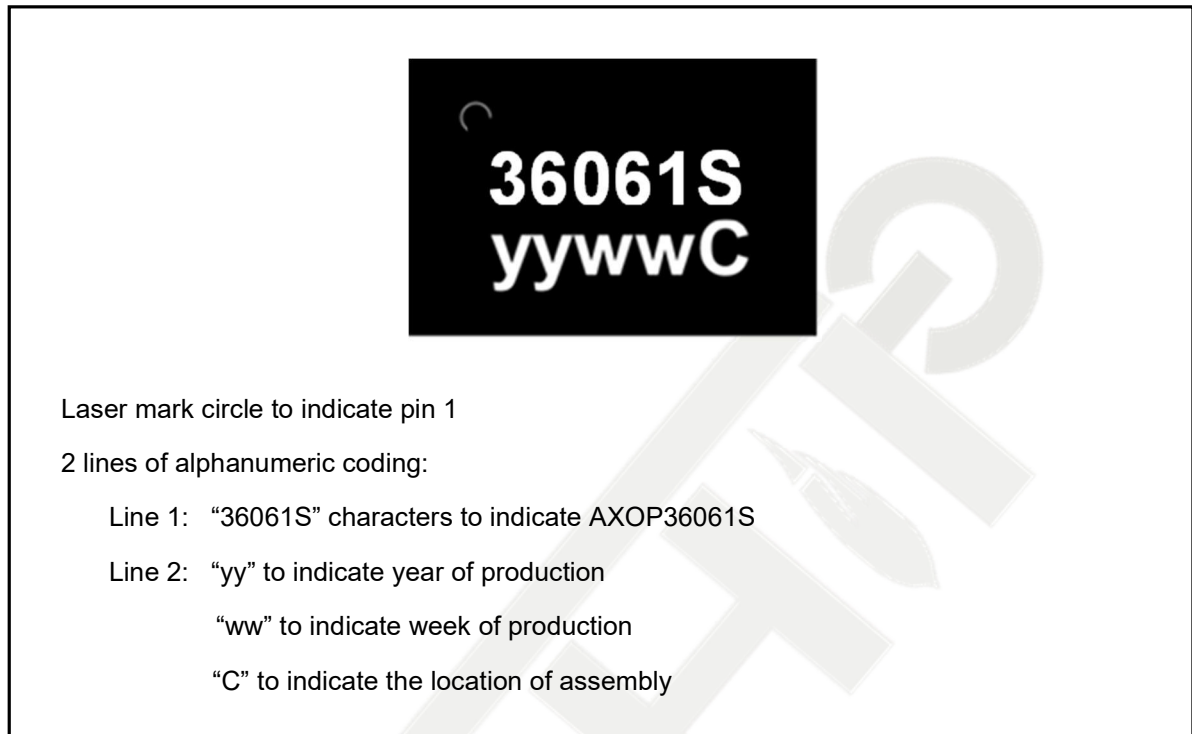
5.1 Package Dimensions

Figure 13 eSOP8 Mechanical Data and Package Dimensions



5.2 Marking Information

Figure 14 eSOP8 Marking Information



6 Packing Information

Figure 15 Packing Information



7 Revision History

Table 6 Document Revision History

Date	Version	Description
Apr 2023	1.00	V1.00 version.